

The BM16 control system with Linux and PCI

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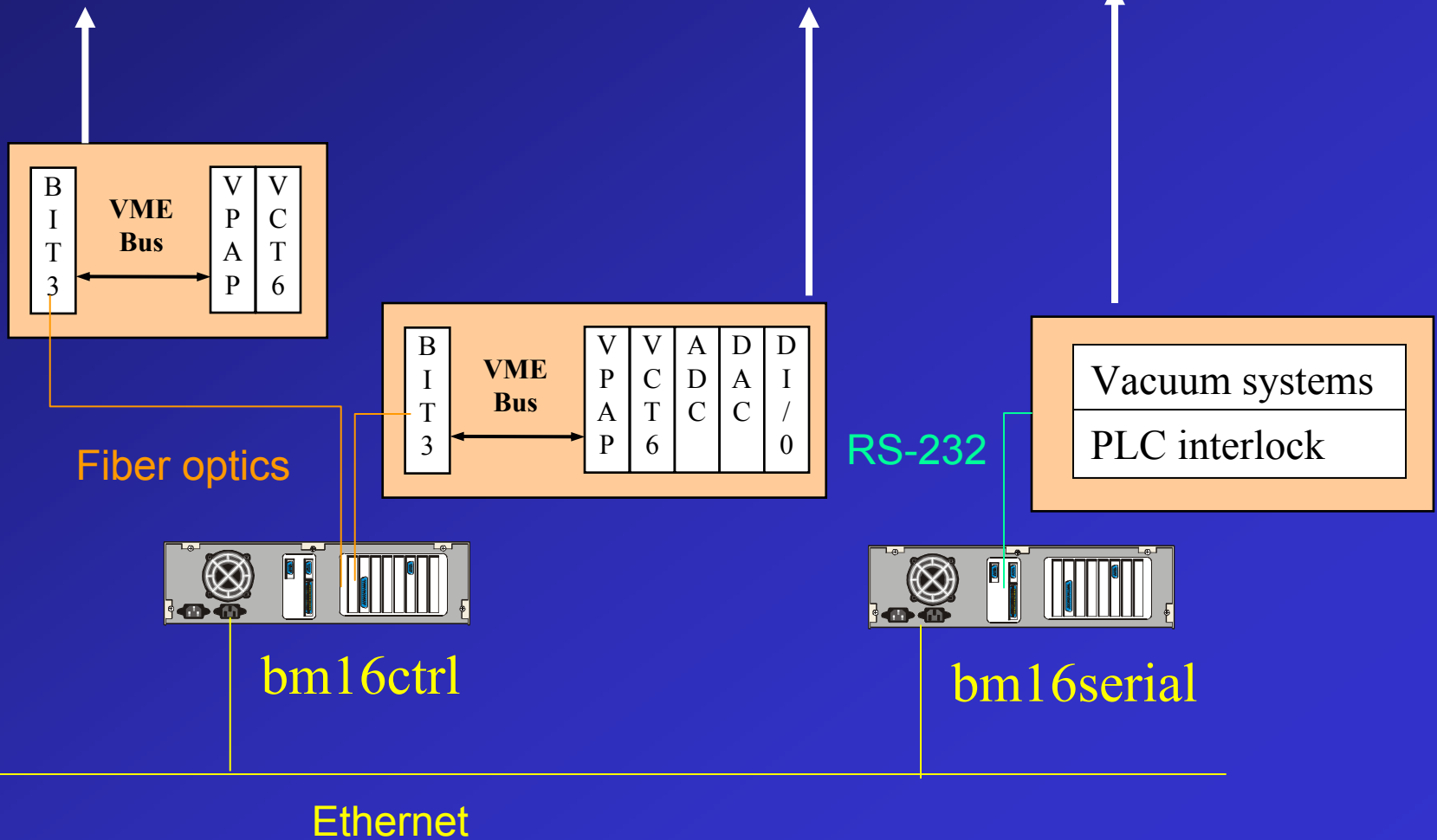
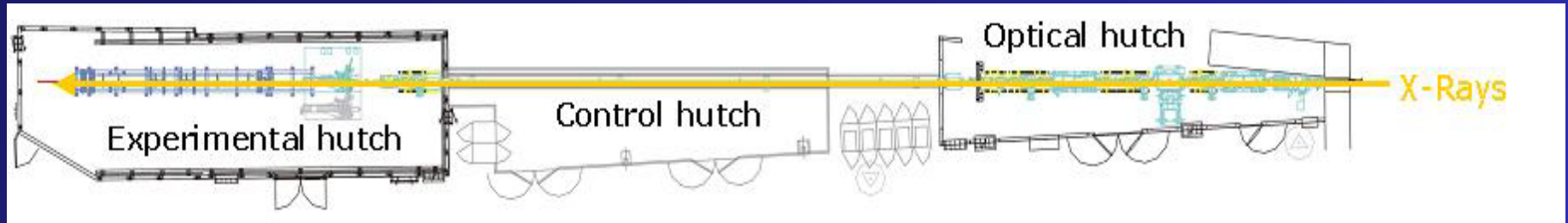
Background

- **Heritage + Upgrade (from 68K/OS9, Solaris)**
- **Commercial products**
- **Compatible (and collaborate) with ESRF**

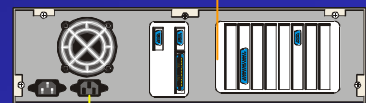
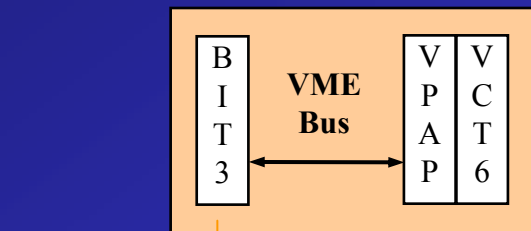
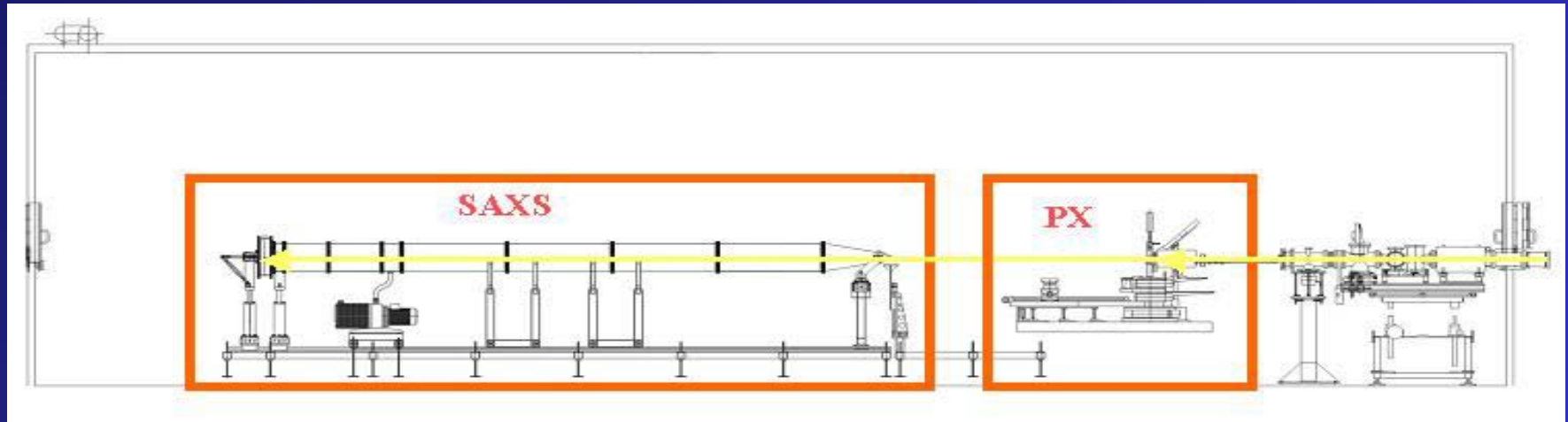
Main components

- **VME, PCI, cPCI, Ethernet devices**
- **Industrial PC**
- **Linux**
- **PCI-VME bus coupler**
- **Taco + Spec**

Layout I (optics hutch control)



Layout II (exp. hutch control)



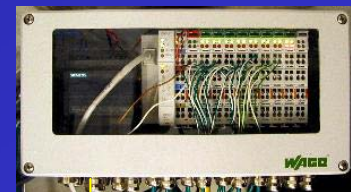
bm16ctrl

RS-232



rocket16

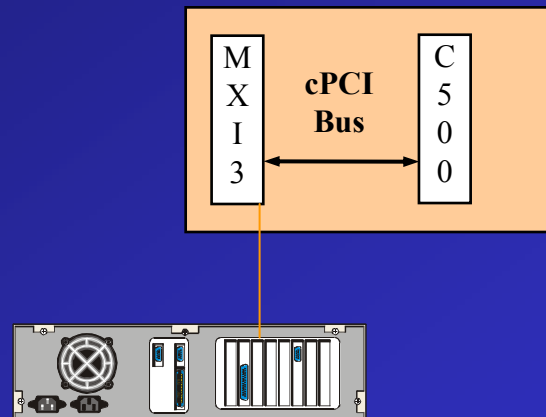
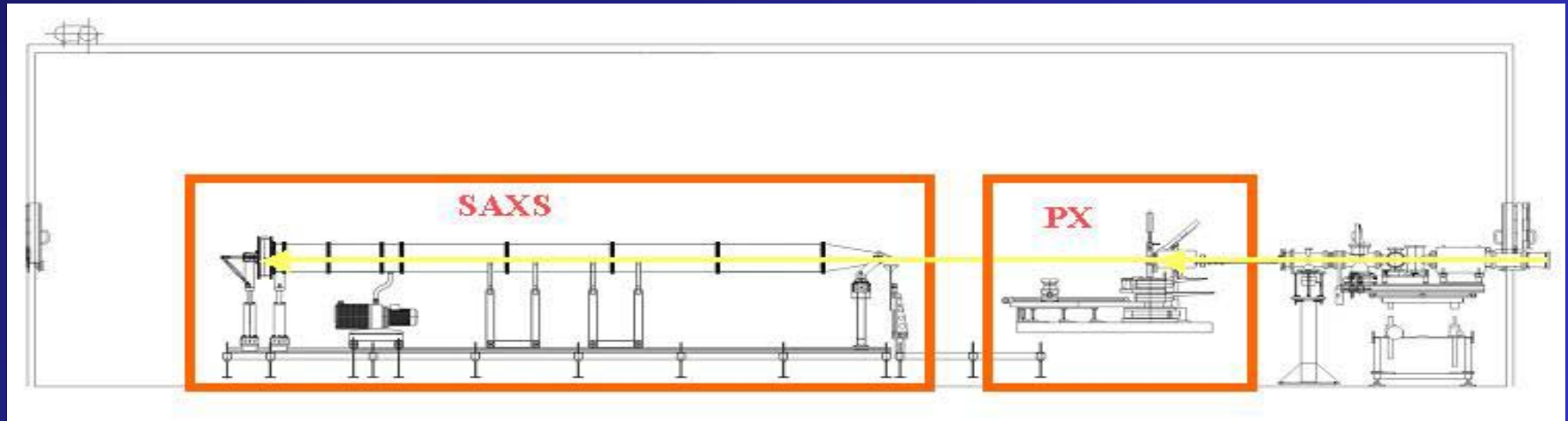
A I/O, D I/O



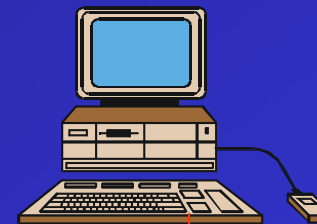
wcbm16a

Ethernet

Layout III (detector control)



bm16saxs



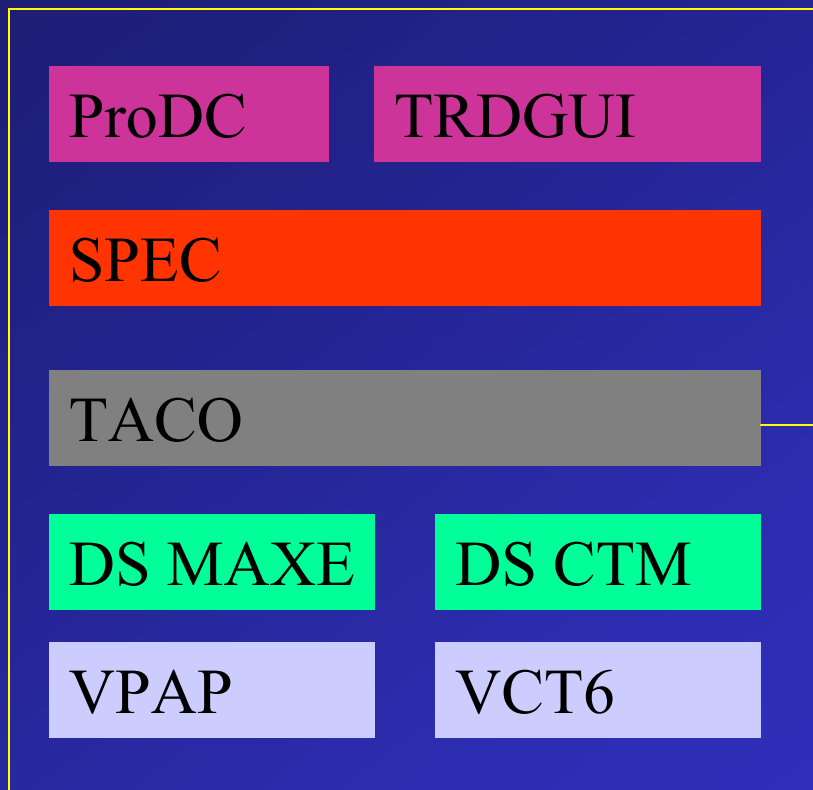
bm16ccdpX

Giga-Ethernet

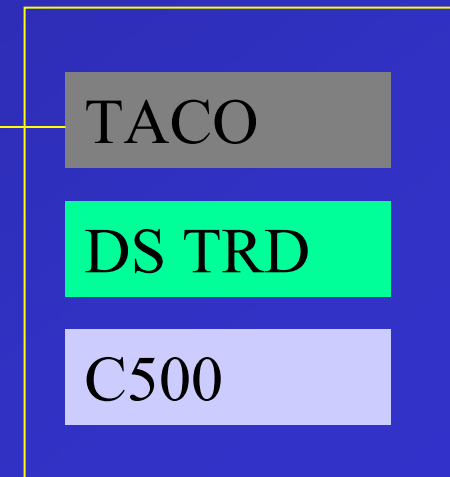
Ethernet

Software architecture

bm16ctrl



bm16saxs



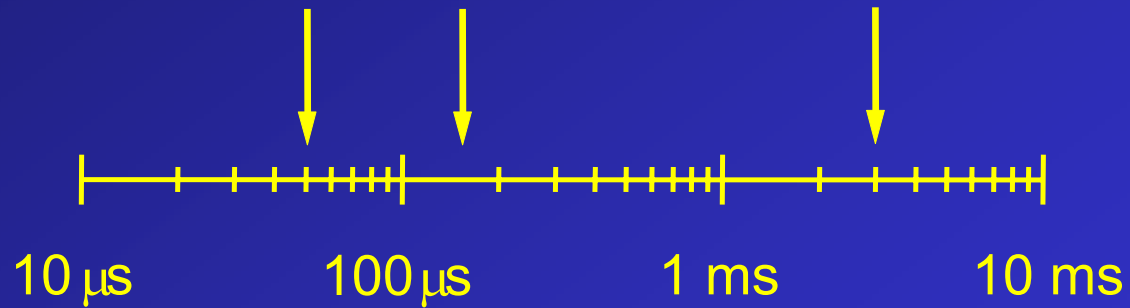
Ethernet

Software components

- Suse 7.2
- 2.4.18 SMP

Software performance

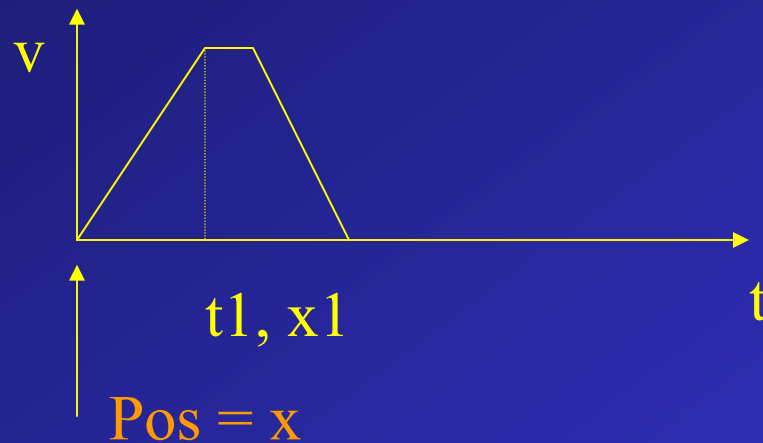
DS on local DS on remote DS on VME
PC: 50 μ s PC: 150 μ s CPU: 3 ms
(X 60) (X 20)



Duration of a single access to the hardware

Automation

- Spec macros
 - Oscillation at constant speed (zap)



Automation

- **Spec macros**
 - Energy scans
 - Quick realignment
 - Xbpm scans

Fast data acquisitions: Hook



VCT6
Counter/Timer

Counting gate



Interrupts

Hook kernel
module

Count &
encoder
channels

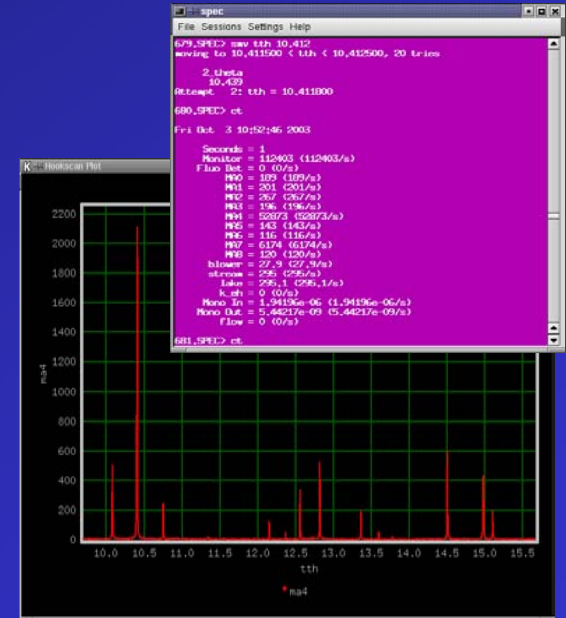
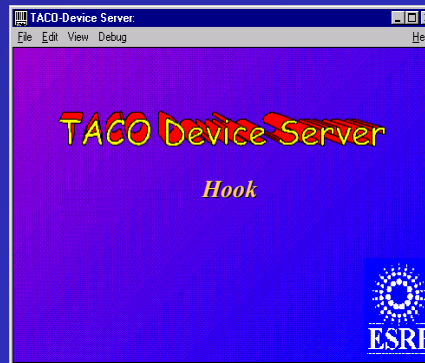
0	Counts	Motor Pos.
1
2

Hook kernel buffer

Flexmotion



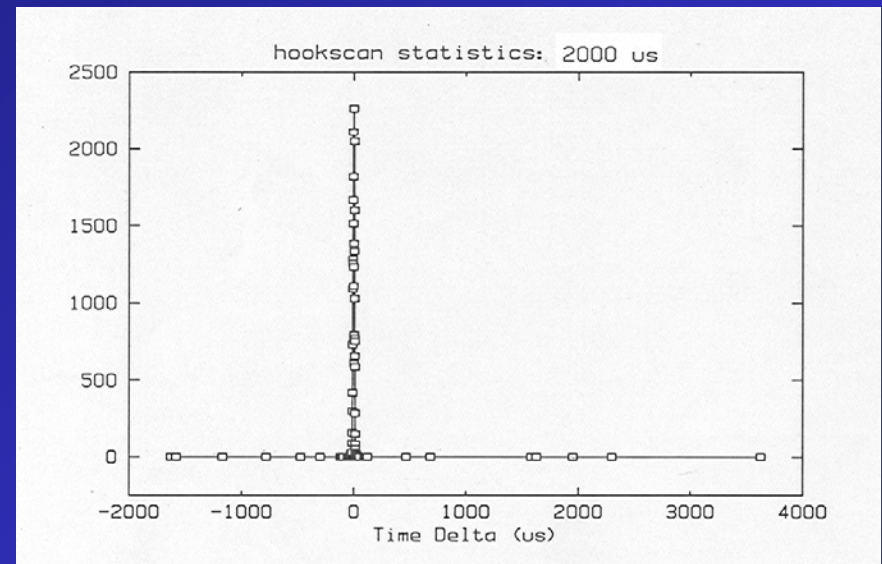
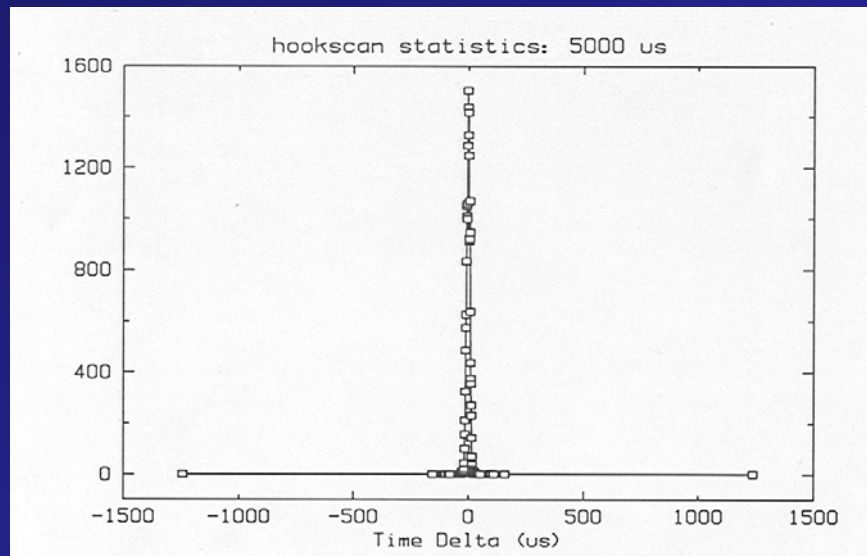
System
call



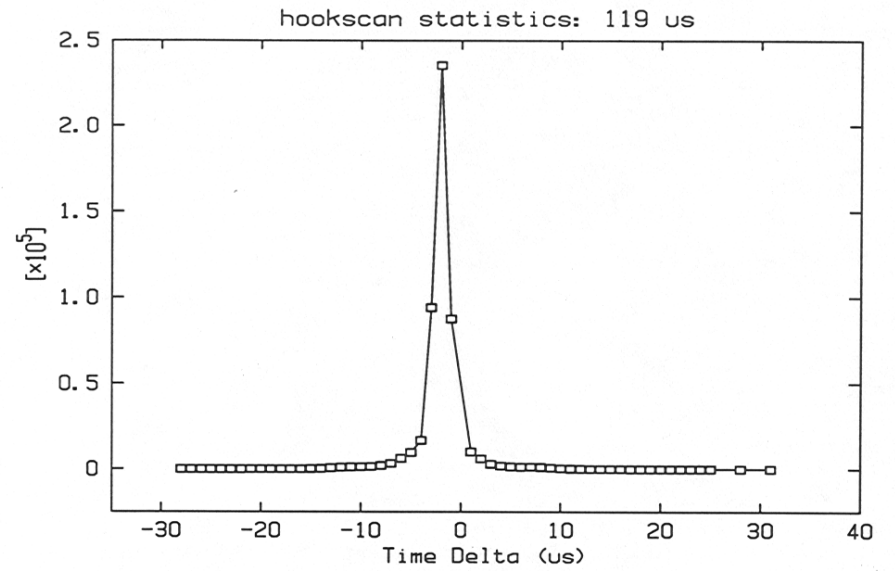
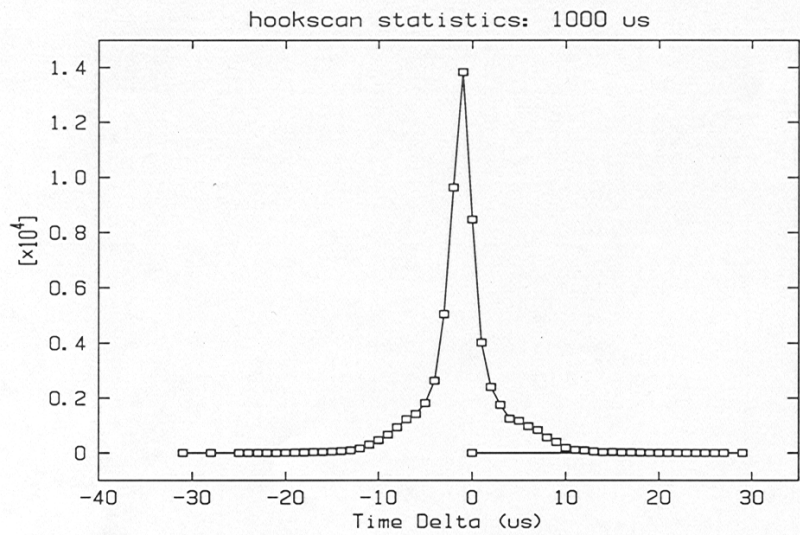
SPEC

TACO/TANGO
call

Single CPU



Dual CPU



Conclusions

- **Beamline control with Linux/PCI**
- **Linux is not real time ...**
 - ... but 2 CPUs help a lot!
- **Multiplied by 20 the limit speed**
- **Flexibility, Maintenance, Upgrade**
- **Automation (spec macros)**